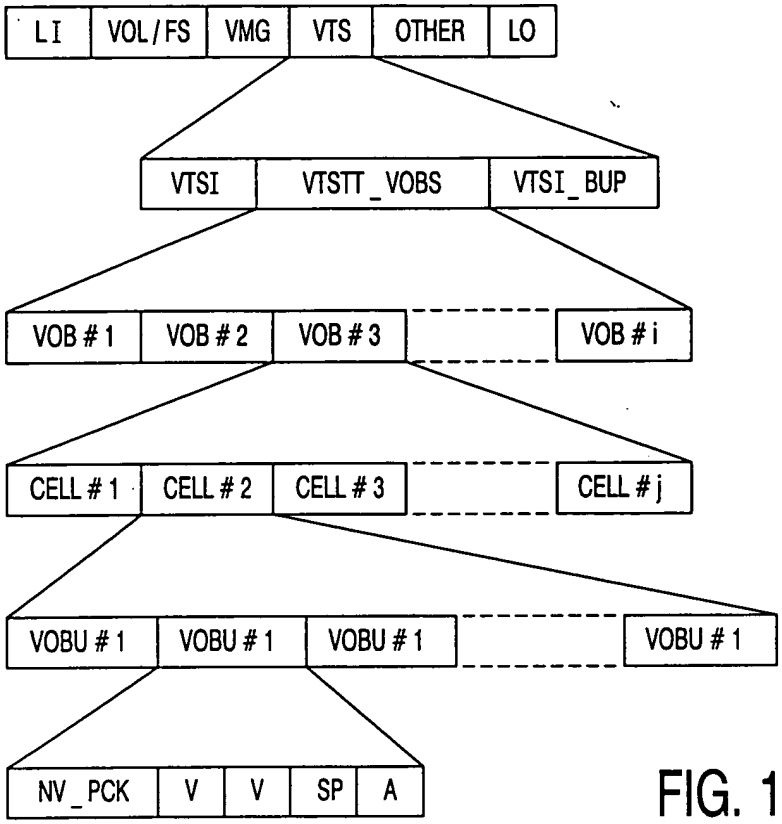

LI
VOL/FS
VMG
VTS
OTHER
LO
VTSI
VTSTT_VOBS
VTSI_BUP
VOB # 1
VOB # 2
VOB # 3
VOB # i
CELL # 1
CELL # 2
CELL # 3
CELL # j
VOBU # 1
VOBU # 1
VOBU # 1
VOBU # 1
NV_PCK
V
V
SP
A

FIG. 1

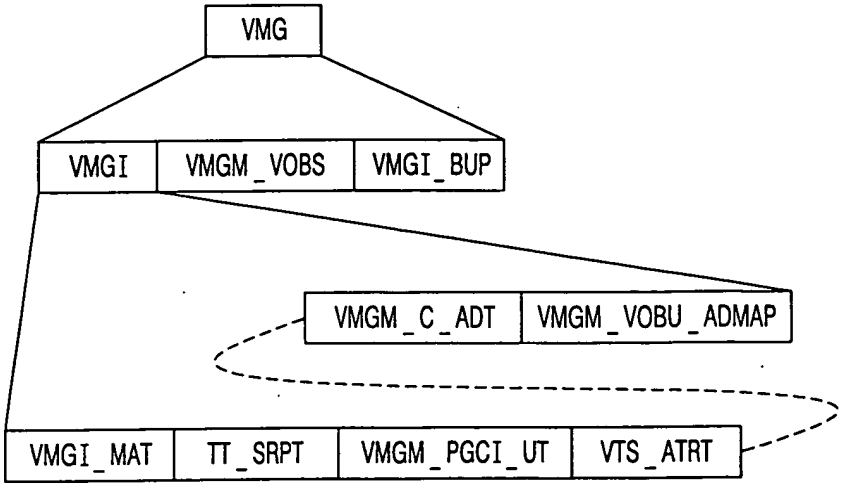

VMG
VMGI
VMGM_VOBS
VMGI_BUP
VMGM_C_ADT
VMGM_VOBU_ADMAP
VMGI_MAT
TT_SRPT
VMGM_PGCI_UT
VTS_ATRT

FIG. 2

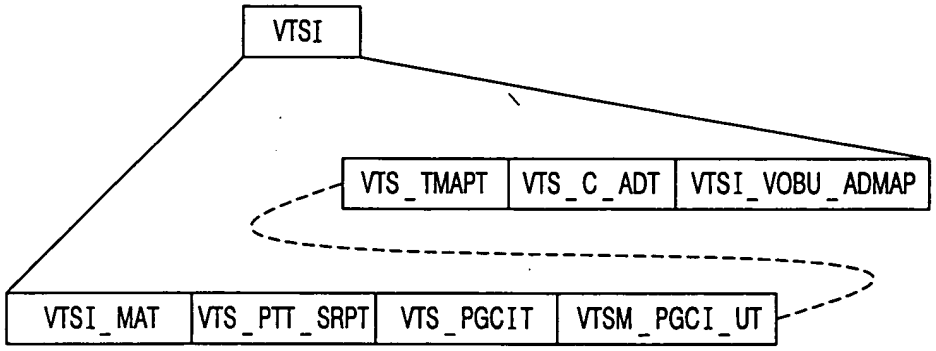



FIG. 3

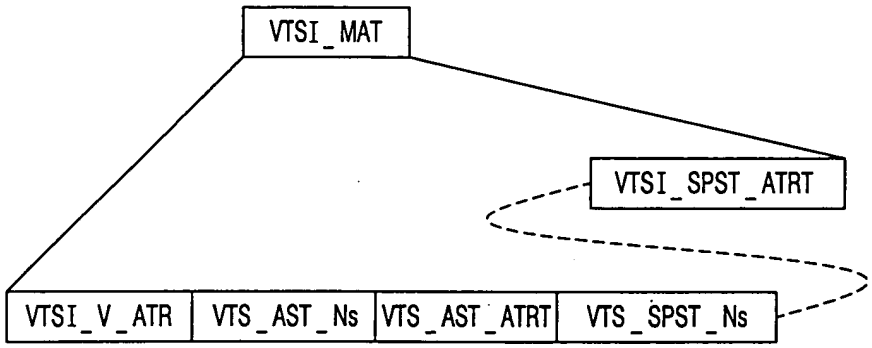



FIG. 4

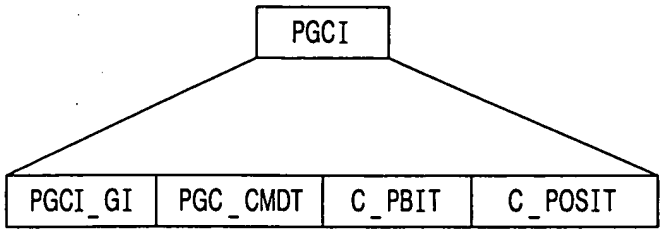
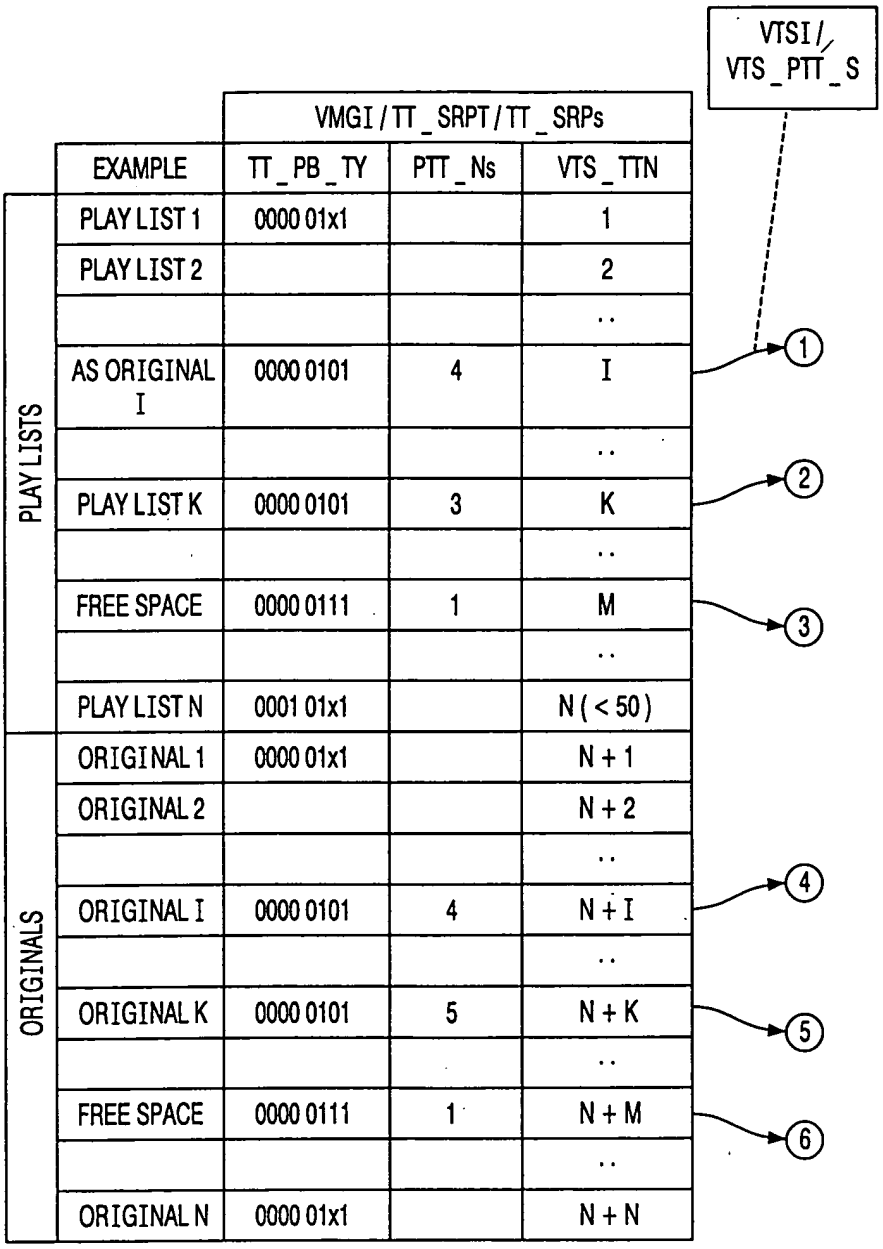



FIG. 5




AGL_Ns == '1'
TT_PTL_ID_FLD == zero
VTSN == '1'

Bit 1 of TT_PB_TY indicates free space

FIG. 6A

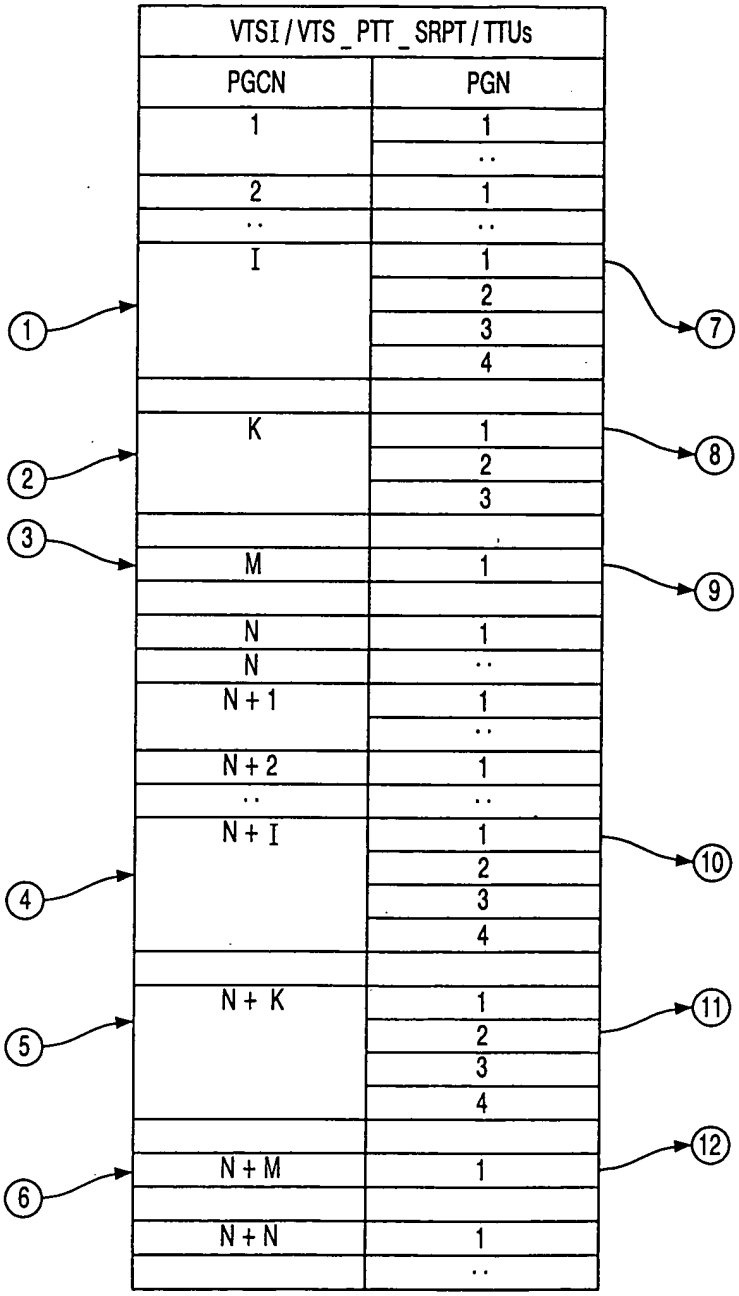
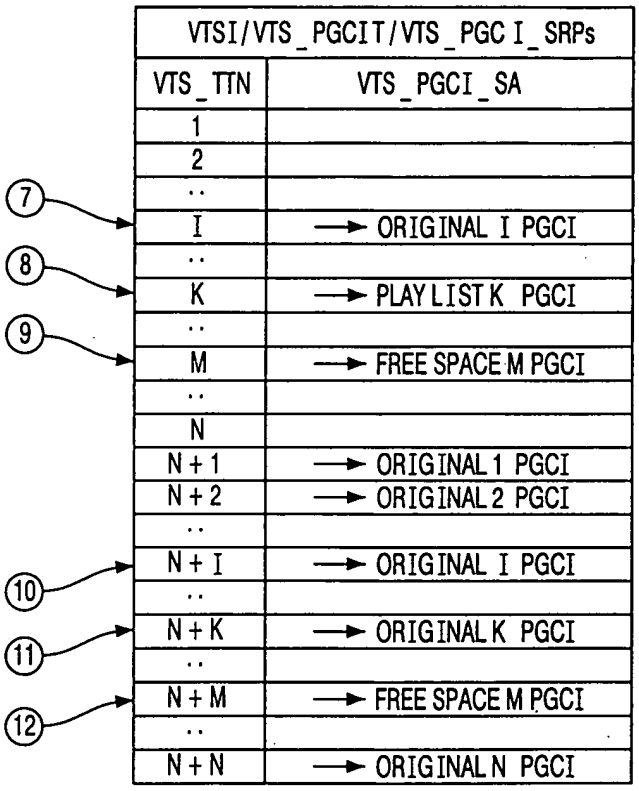

VTSI / VTS _ PTT _ SRPT / TTUs
PGCN
PGN
1
1
..
2
1
..
..
I
1
2
3
4
K
1
2
3
M
1
N
1
N
..
N + 1
1
..
N + 2
1
..
..
N + I
1
2
3
4
N + K
1
2
3
4
N + M
1
N + N
1
..
1
2
3
4
5
6
7
8
9
10
11
12

FIG. 6B




Entry type == 1
Block mode == 00b
Block type == 00b
PTL_ID_FLD == 0000h

Equal VTS_PGCI_SA indicate "no THL"

FIG. 6C

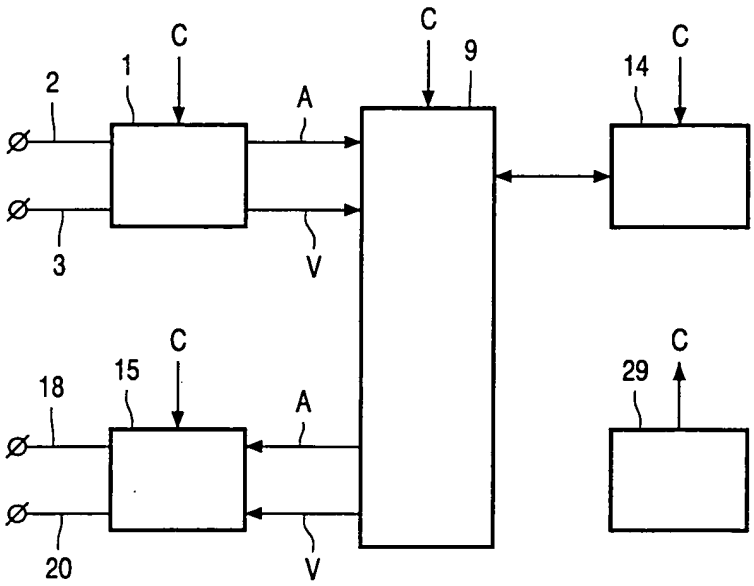



FIG. 7

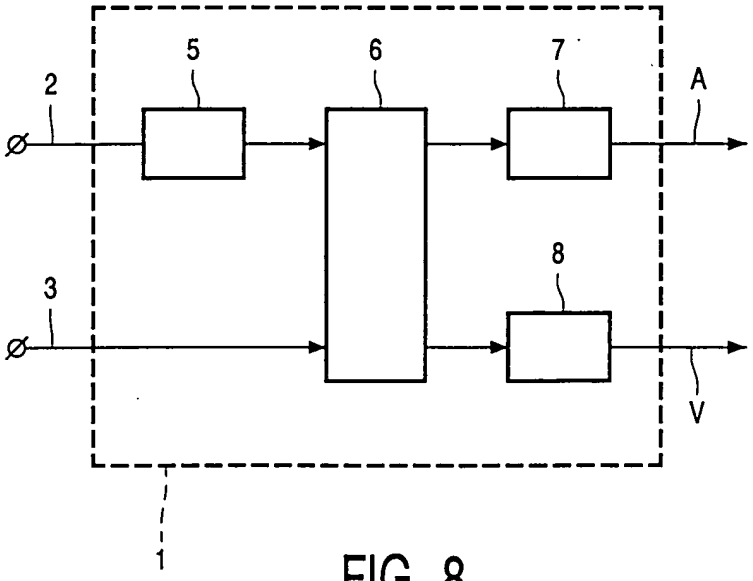



FIG. 8

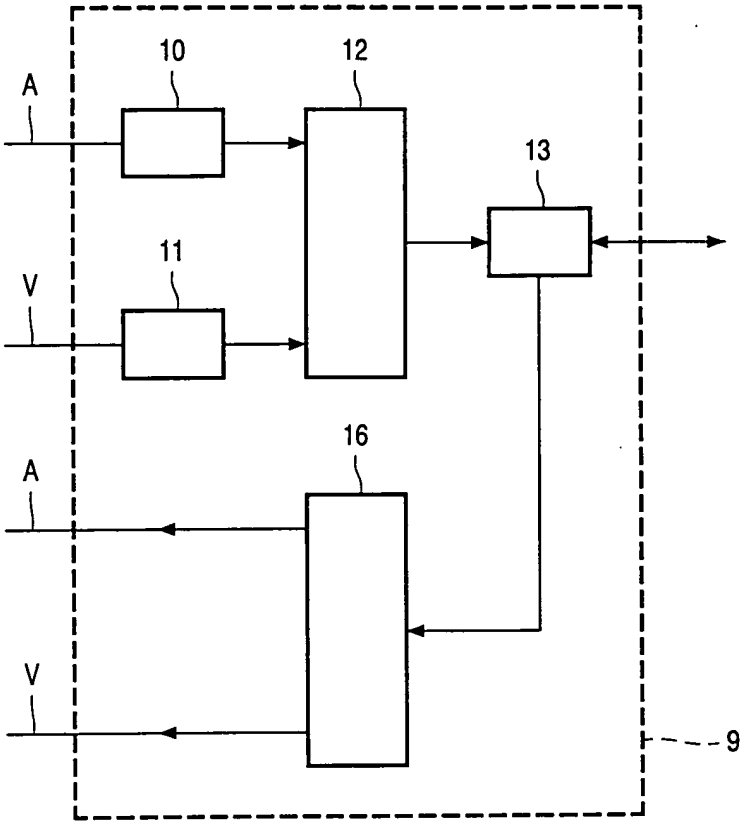



FIG. 9

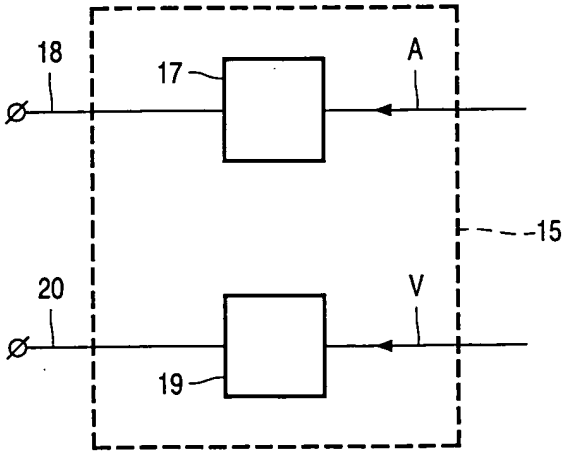



FIG. 10

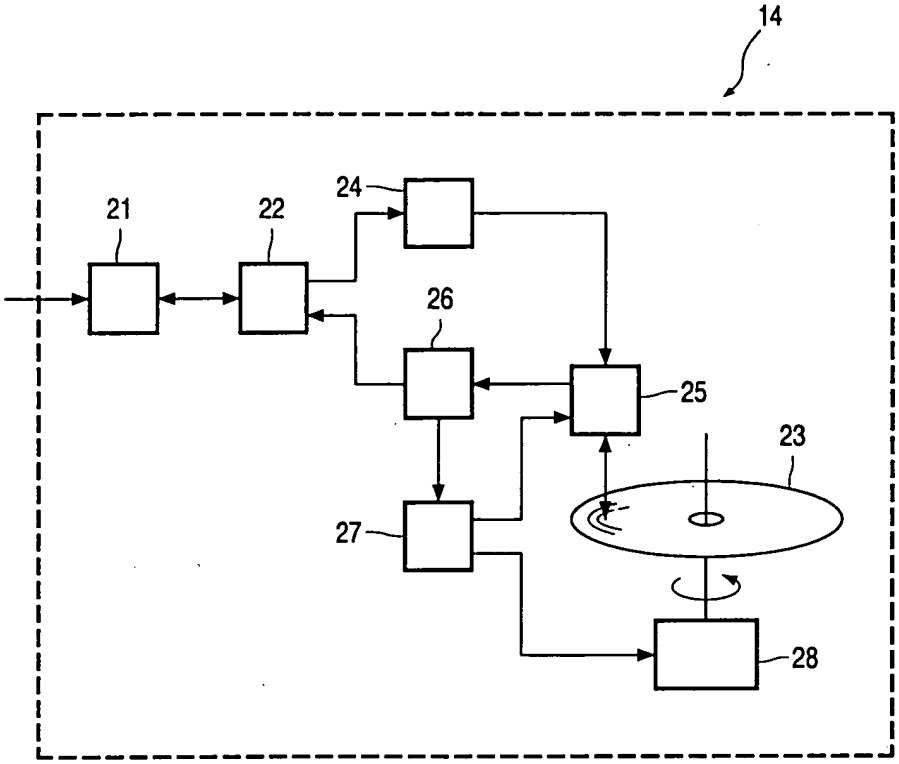



FIG. 11

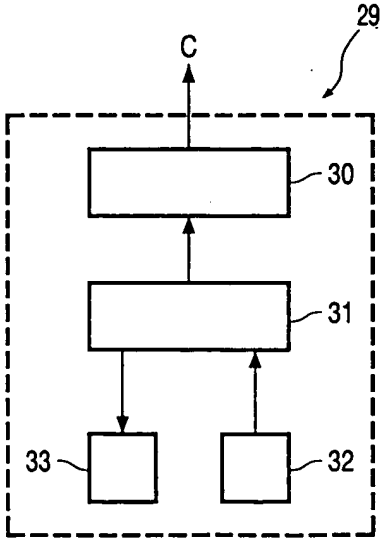



FIG. 12